## REVISION HISTORY

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## REVISION HISTORY

**Design Name**: ORANGE Pi Zero plus

**Size**: A3

**Page Name**: Sheet 1

**Date**: Tuesday, March 07, 2017

**Xunlong Software**
DCIN

POWER

Vout = 0.6*(1+Rd1/Rd2)
VDRAM=1.5V/1A, Rd2=100K-1%
VDRAM=1.35V/1A, Rd1=120K-1%

SYSTEM 1.2V/2.5A
Vout = 0.6*(1+R5/R6)

CPUX 1.1V/1.3V/3A
Vout = 0.6*(1+R1/R2)

AVCC 3.3V/1A

WIFI Power 3.3V/300mA
note: Make sure the routing between the ESD and the USB connectors should be on the same PCB side.
Note 1: The Trace length between La and PHY's Pin48 must be within 0.5 cm. CJ(22uF) and CK(0.1uF) to La must be within 0.5cm.

Note 2: The Trace length from CA(22uF),CB(0.1uF) to Pin 44,45(VDDREG) must be within 0.5 cm. The trace width from AVDD33 to Pin 44,45 should be 40mil.

PHY Address=001(RTL8211E)

Full down for 2.5V RGMI(RTL8211B/B211E)

Config for all capability

Add TX/RX Delay

LED2: Blinking=Transmitting or Receiving,
LED1: Link Up/Down