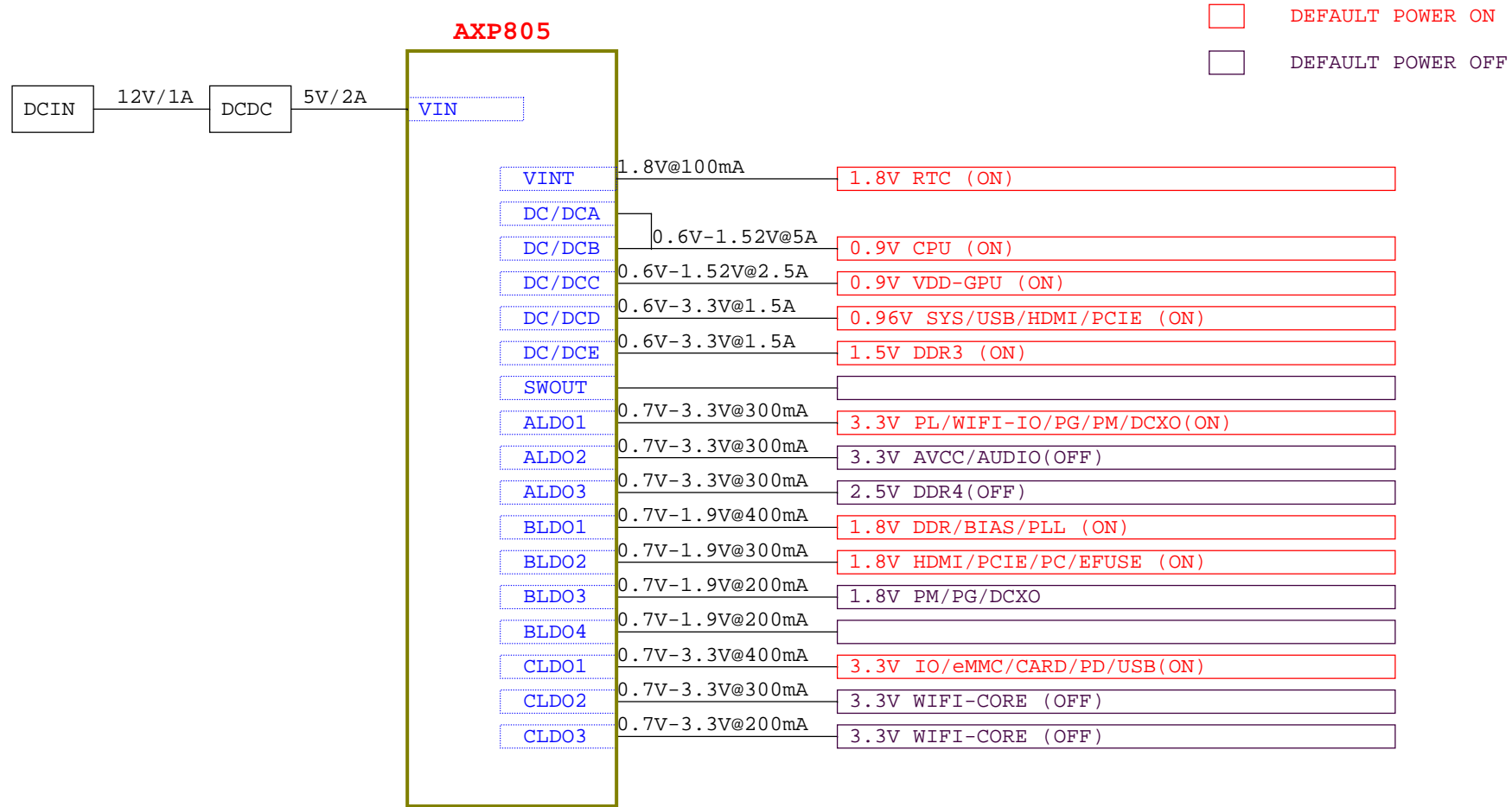


# POWER TREE



# GPIO ASSIGNMENT

PIN	Define	CFG	Function
PC0	NAND_WE	2	NAND/eMMC
PC1	NAND_ALE/SDC2_DS	2/3	
PC2	NAND_CLE	2	
PC3	NAND_CE0	2	
PC4	NAND_RE/SDC2_CLK	2/3	
PC5	NAND_RB0/SDC2_CMD	2/3	
PC6	NAND_DQ0/SDC2_D0	2/3	
PC7	NAND_DQ1/SDC2_D1	2/3	
PC8	NAND_DQ2/SDC2_D2	2/3	
PC9	NAND_DQ3/SDC2_D3	2/3	
PC10	NAND_DQ4/SDC2_D4	2/3	
PC11	NAND_DQ5/SDC2_D5	2/3	
PC12	NAND_DQ6/SDC2_D6	2/3	
PC13	NAND_DQ7/SDC2_D7	2/3	
PC14	NAND_DQS/SDC2_RST	2/3	
PC15	NAND_CE1	2	
PC16	NAND_RB1	2	

PIN	Define	CFG	Function
PD0			
PD1			
PD2			
PD3			
PD4			
PD5			
PD6			
PD7			
PD8			
PD9			
PD10			
PD11			
PD12			
PD13			
PD14			

PIN	Define	CFG	Function
PD15			
PD16			
PD17			
PD18			
PD19			
PD20			
PD21			
PD22			
PD23			
PD24			
PD25			
PD26			

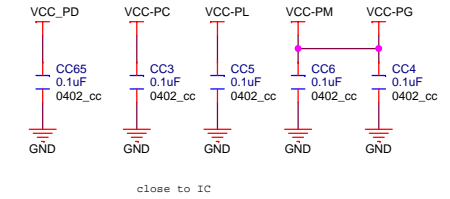
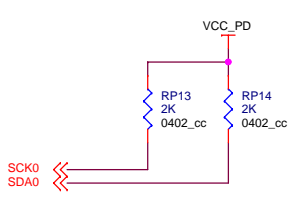
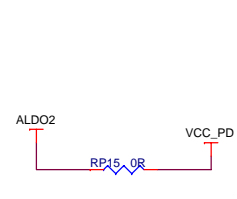
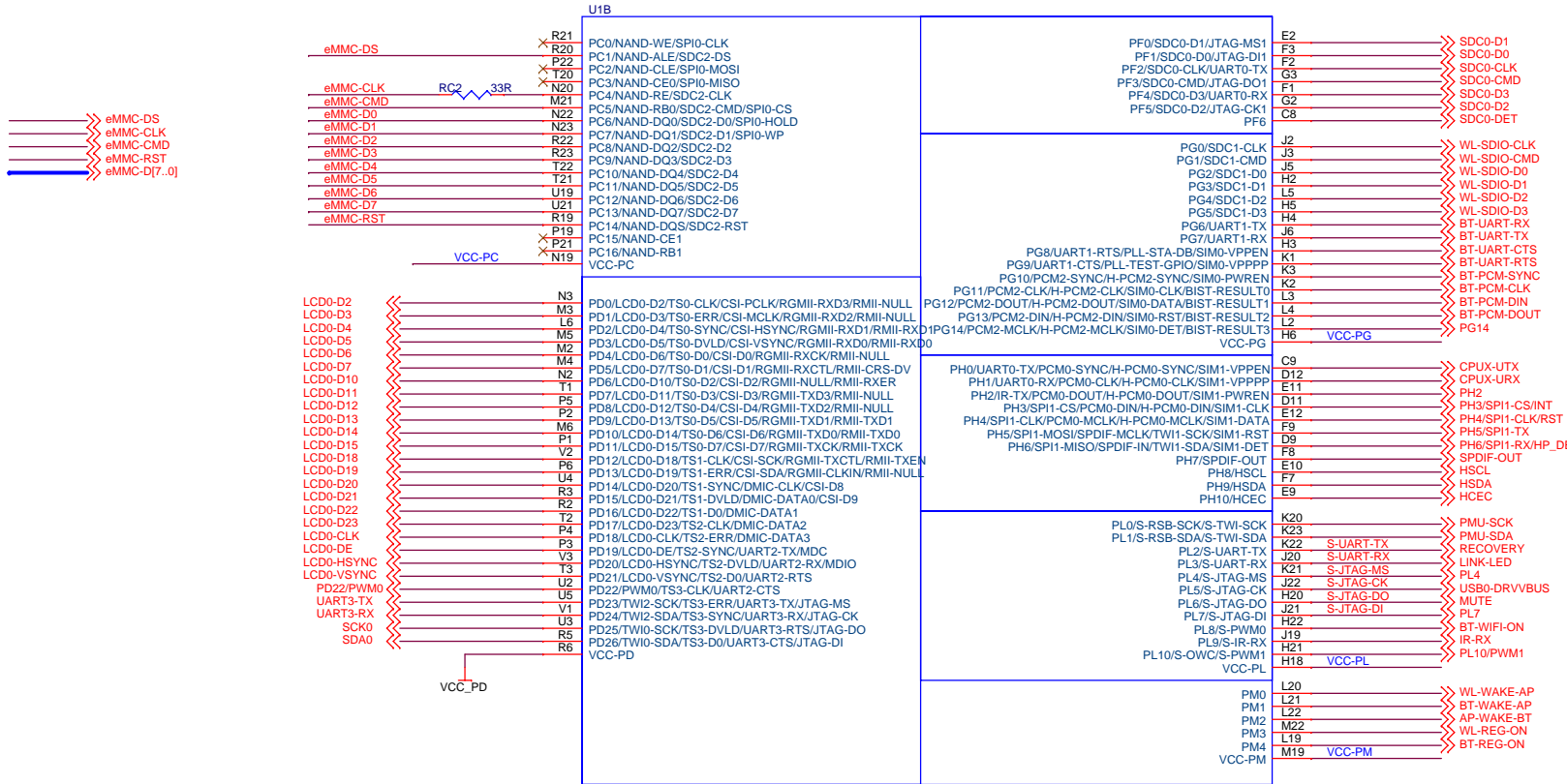
PIN	Define	CFG	Function
PG0	SDC1_CLK	2	WIFI+BT
PG1	SDC1_CMD	2	
PG2	SDC1_D0	2	
PG3	SDC1_D1	2	
PG4	SDC1_D2	2	
PG5	SDC1_D3	2	
PG6	UART1_TX	2	
PG7	UART1_RX	2	
PG8	UART1_RTS	2	
PG9	UART1_CTS	2	
PG10	PCM2_SYNC	2	
PG11	PCM2_CLK	2	
PG12	PCM2_DOUT	2	
PG13	PCM2_DIN	2	
PG14			

PIN	Define	CFG	Function
PF0	SDC0_D1	2	CARD0
PF1	SDC0_D0	2	
PF2	SDC0_CLK/UART0_TX	2/3	
PF3	SDC0_CMD	2	
PF4	SDC0_D3/UART0_RX	2/3	
PF5	SDC0_D2	2	
PF6	SDC0-DET	2	

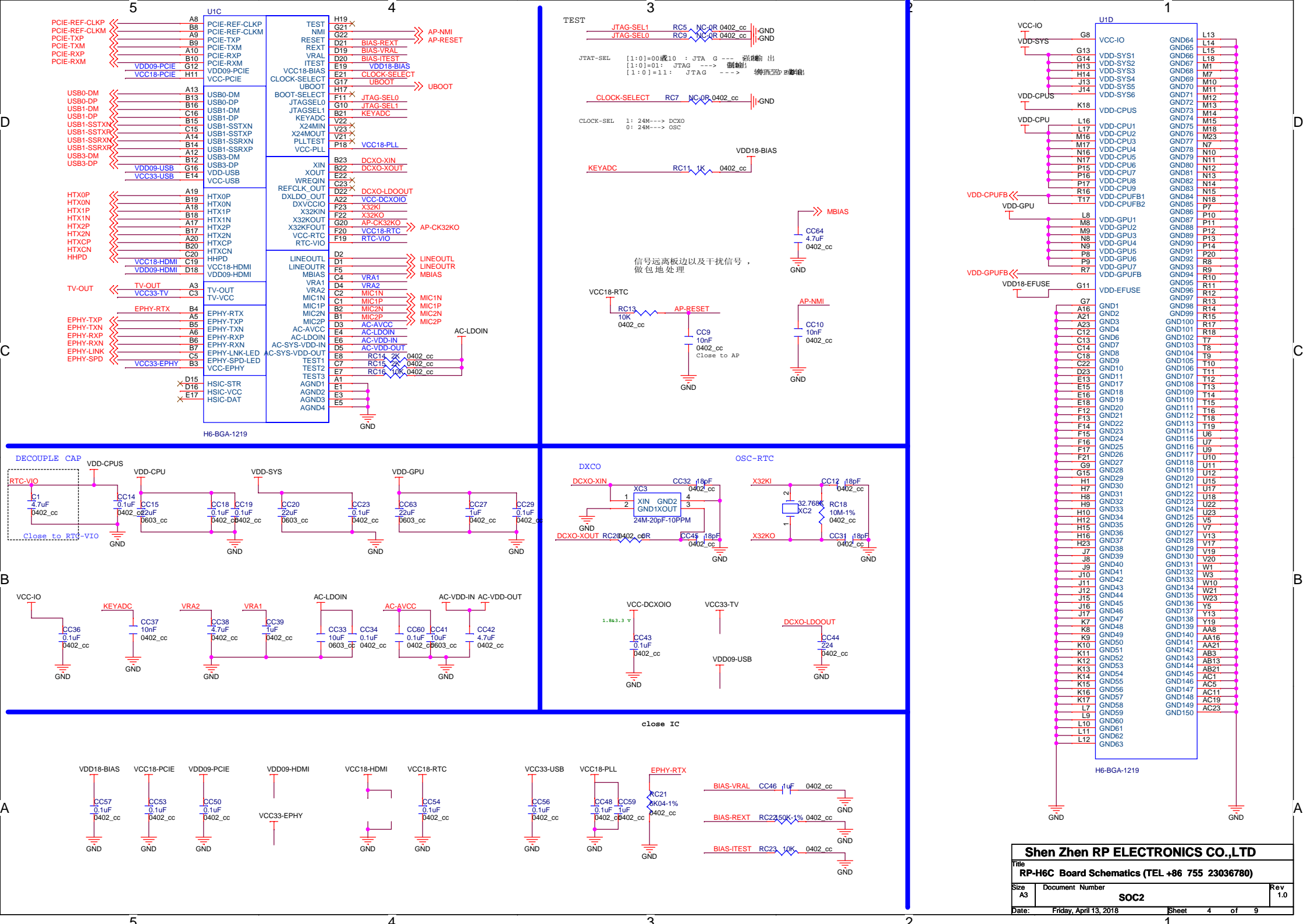
PIN	Define	CFG	Function
PH0	CPUX-UTX	2	HDMI
PH1	CPUX-URX	2	
PH2	DDC_CEC_EN	6	
PH3			
PH4			
PH5			
PH6			
PH7	SPDIF_OUT	3	
PH8	HSCL	2	
PH9	HSDA	2	
PH10	HCEC	2	

PIN	Define	CFG	Function
PL0	PMU-SCK	3	WIFI+BT
PL1	PMU-SDA	3	
PL2	RECOVERY	6	
PL3	LINK-LED	1	
PL4	PWR-LED	1	
PL5	USB0-DRVVBUS	1	
PL6	MUTE	1	
PL7	STATUS-LED	1	
PL8	BT-WIFI-ON	1	
PL9	IR-RX	2	
PL10			
PM0	WL-WAKE-AP	6	
PM1	BT-WAKE-AP	6	
PM2	AP-WAKE-BT	1	
PM3	WL-REG-ON	1	
PM4	BT-REG-ON	1	

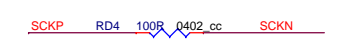
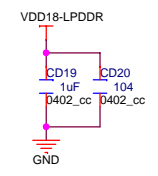
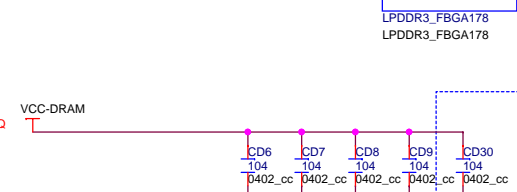
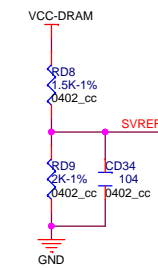
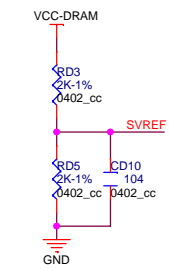
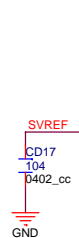
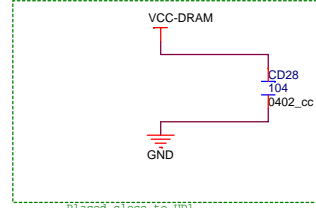
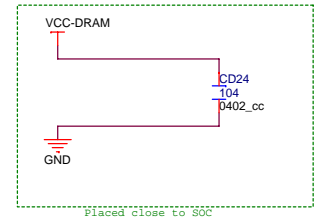
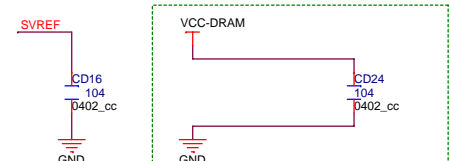
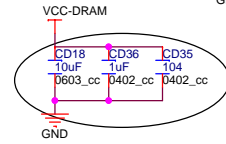
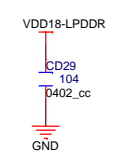
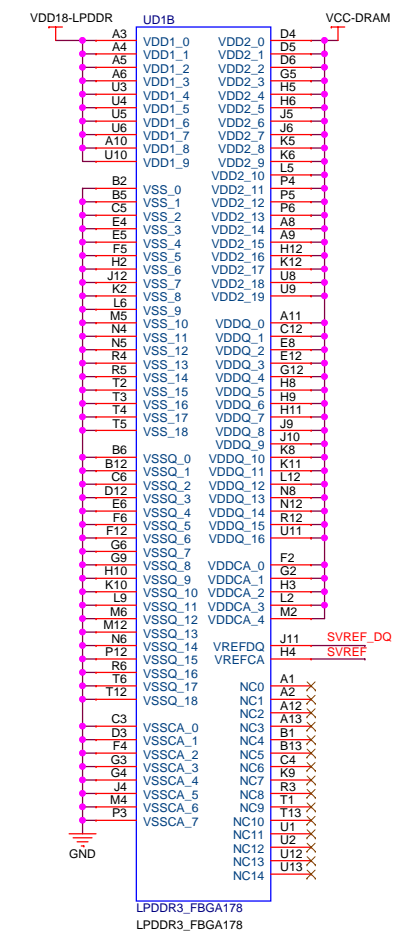
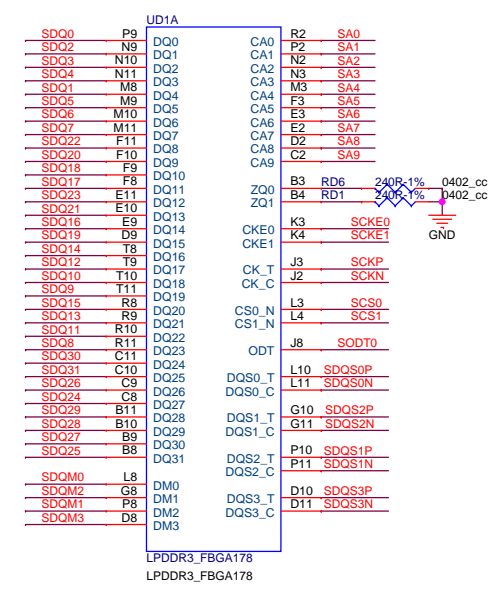
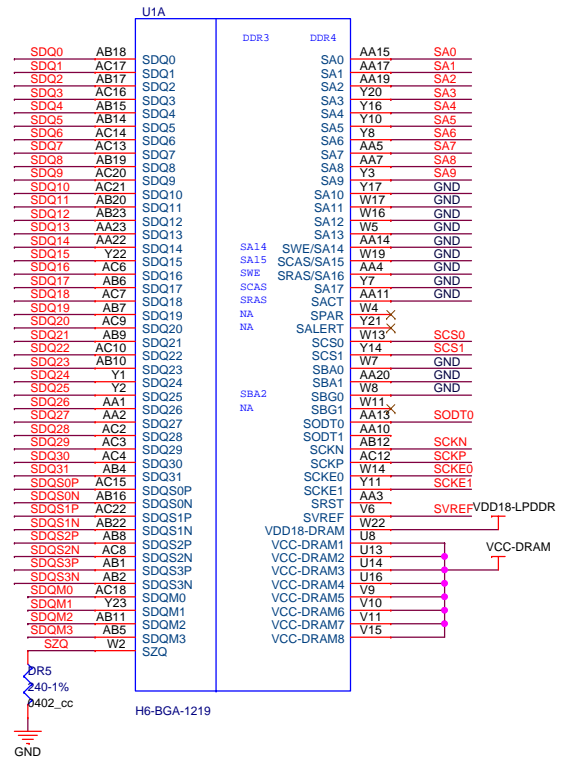
PC, PD, 部分引脚保留功能  
PF, PG, PH, PL, PM, 部分引脚保留功能



close to IC



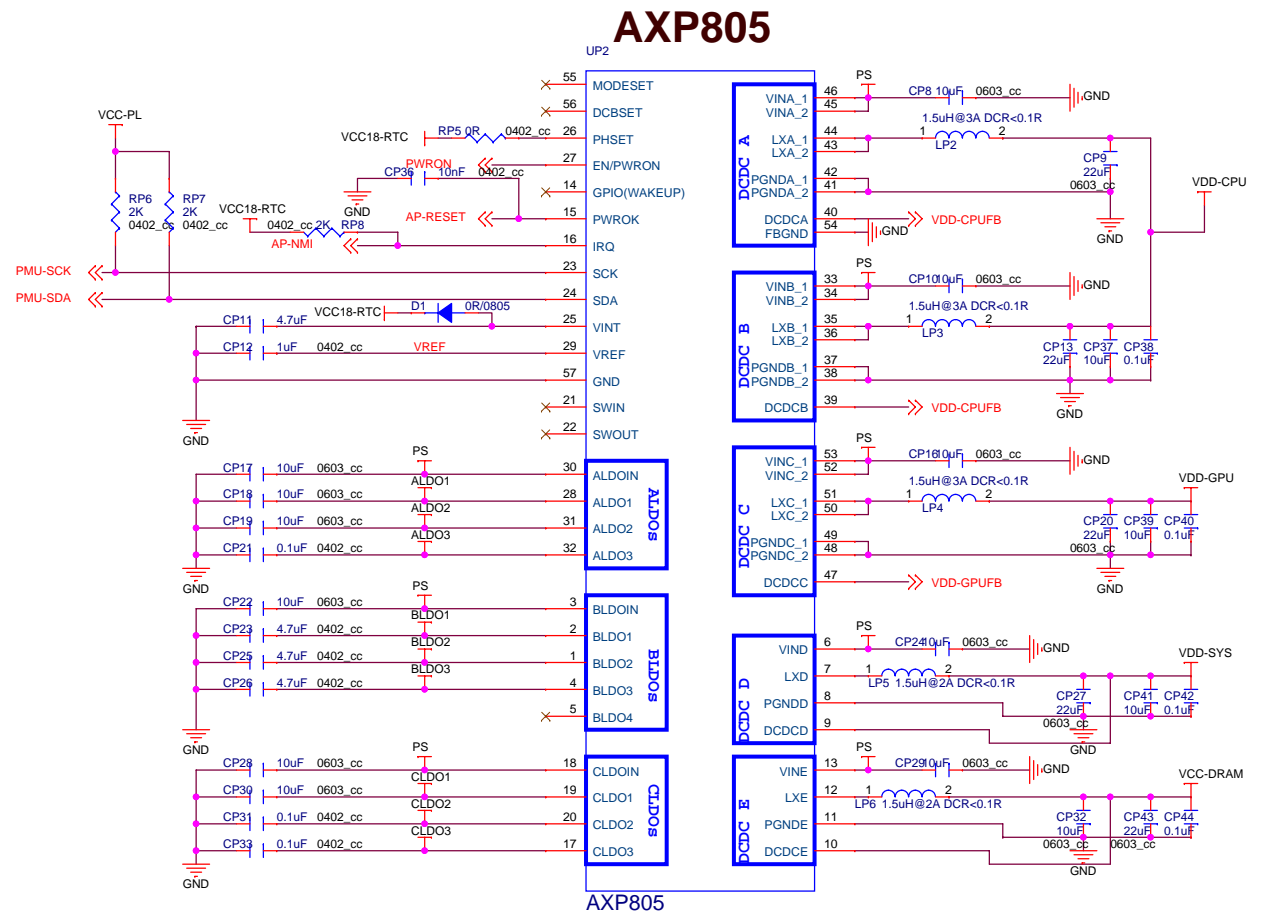
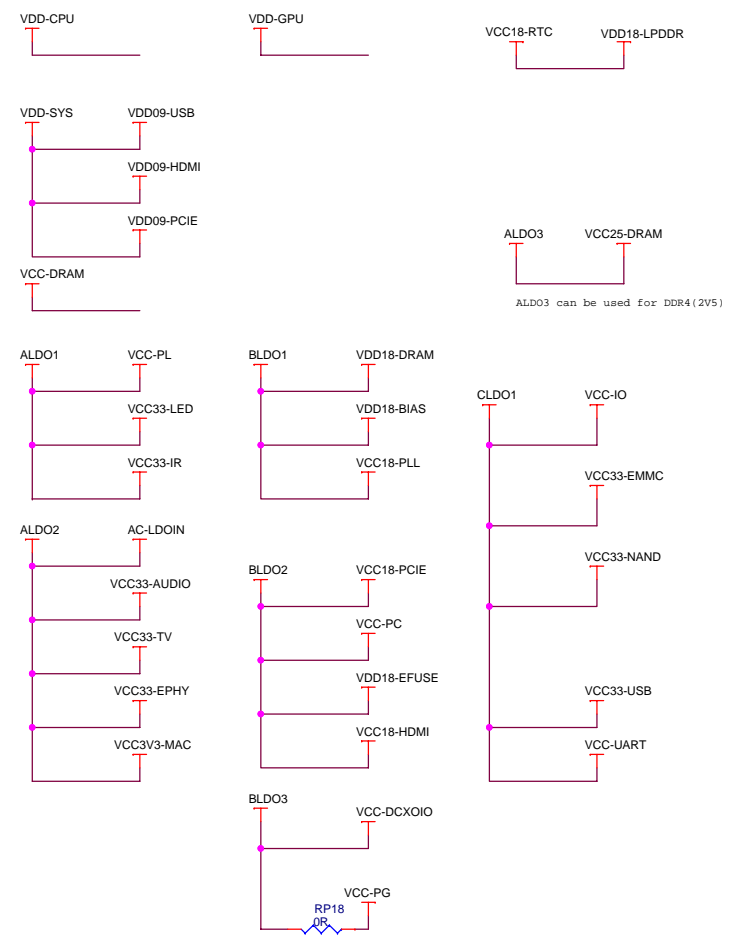
# LPDDR3



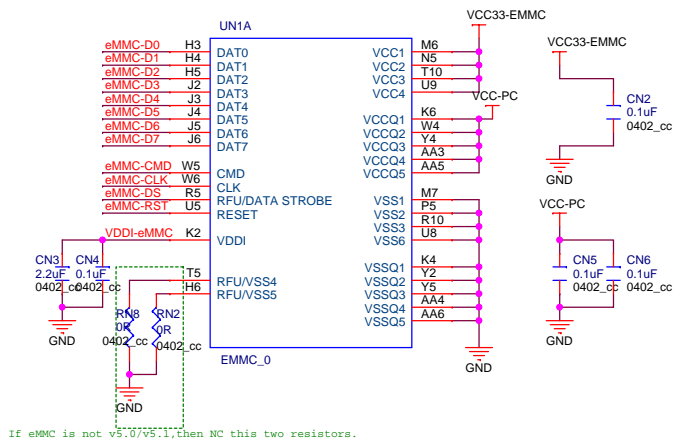
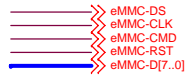
靠近过孔换层的地方



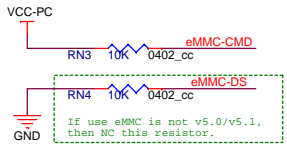
# PMIC



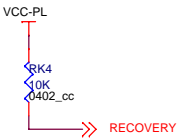
# eMMC



If eMMC is not v5.0/v5.1, then NC this two resistors.



Placed close to SOC



UN1B		EMMC_0	
A4	NC1	NC88	P13
A6	NC2	NC69	P14
A9	NC3	NC70	R1
A11	NC4	NC71	R2
B2	NC5	NC72	R3
B13	NC6	NC73	R12
D1	NC7	NC74	R13
D14	NC8	NC75	R14
H1	NC9	NC76	T1
		NC77	T2
		NC78	T12
H8	NC12	NC79	T13
H9	NC13	NC80	T14
H10	NC14	NC81	U1
H11	NC15	NC82	U2
H12	NC16	NC83	U3
H13	NC17	NC84	U6
H14	NC18	RFU10(NC85)	U7
J1	NC19	RFU11(NC86)	U10
J7	NC20	RFU12(NC87)	U12
J8	NC21	NC88	U13
J9	NC22	NC89	U14
J10	NC23	NC90	V1
J11	NC24	NC91	V2
J12	NC25	NC92	V3
J13	NC26	NC93	V12
J14	NC27	NC94	V13
		NC95	V14
		NC96	V1
		NC97	W1
K7	NC31	NC98	W3
K8	NC32	NC99	W7
K9	NC33	NC100	W8
K10	NC34	NC101	W9
K11	NC35	NC102	W10
K12	NC36	NC103	W11
K13	NC37	NC104	W12
K14	NC38	NC105	W13
L1	NC39	NC106	W14
L2	NC40		
L3	NC41		
L4	NC42	NC109	Y3
L12	NC43	NC110	Y6
L13	NC44	NC111	Y7
L14	NC45	NC112	Y8
M1	NC46	NC113	Y9
M2	NC47	NC114	Y10
M3	NC48	NC115	Y11
M12	NC49	NC116	Y12
M13	NC50	NC117	Y13
M14	NC51	NC118	Y14
M5	NC52	NC119	AA1
M8	RFU3(NC52)	NC120	AA2
M9	RFU4(NC53)	NC121	AA7
M10	RFU5(NC54)	RFU13(NC121)	AA8
N1	RFU6(NC55)	NC122	AA9
N2	NC56	NC123	AA10
N3	NC57	RFU14(NC124)	AA11
N10	NC58	NC125	AA12
N12	RFU7(NC59)	NC126	AA13
N13	NC60	NC127	AA14
N14	NC61	NC128	AE1
P1	NC62	NC129	AE14
P2	NC63	NC130	AG2
P3	NC64	NC131	AG13
P10	RFU8(NC65)	NC132	AH4
P12	RFU9(NC66)	NC133	AH6
	NC67	NC134	AH9
		NC135	AH11
		NC136	
		NC137	



